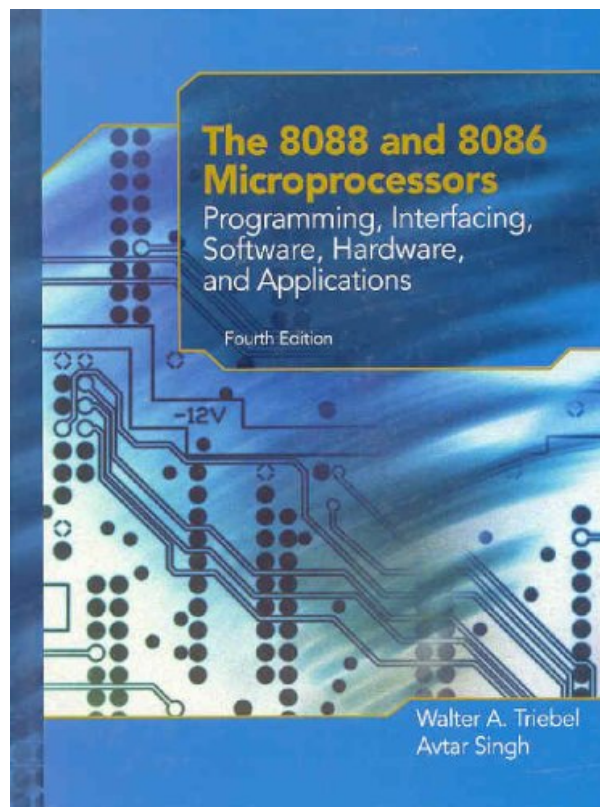
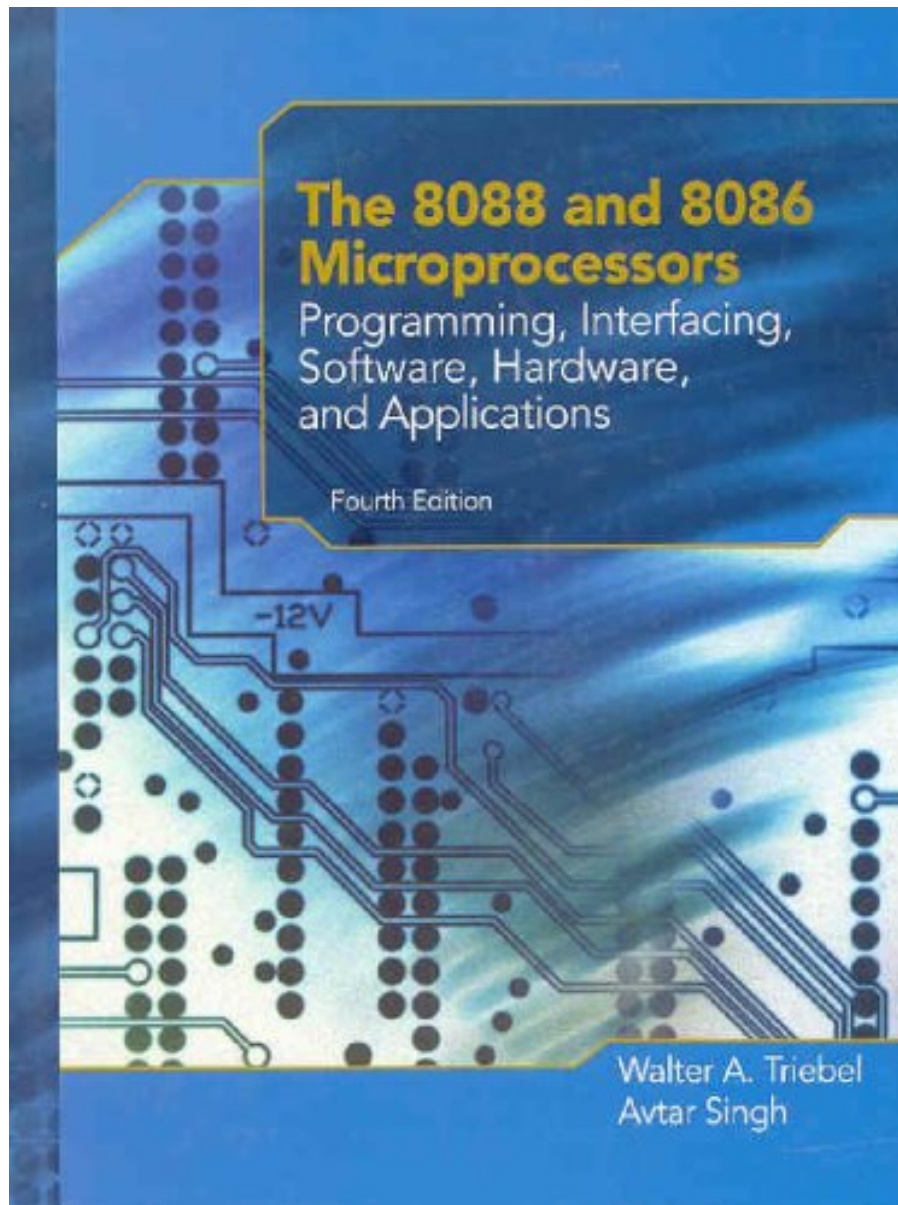


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Designers of microprocessor-based electronic equipment need a systems-level understanding of the 80x86 microcomputer. This volume offers thorough, balanced, and practical coverage of both software and hardware topics. Develops basic concepts using the 8088 and 8086 microprocessors, but the 32-bit version of the 80x86 family is also discussed. Examines how to assemble, run, and debug programs, and how to build, test, and troubleshoot interface circuits. Provides detailed coverage of floating-point processing and the single instruction multiple data (SIMD) processing capability of the advanced Pentium processor. Includes added material on number systems, logic functions and operations, conversion between number systems, and addition/subtraction of binary numbers. Includes new advanced material such as floating Point Architecture and Instructions, Multimedia (MMX) Architecture and Instructions, and the hardware and hardware architecture of the Pentium 3 and Pentium 4 processors. Covers the Intel architecture microprocessor families: 8088, 8086, 80286, 80386, 80486, and the latest Pentium® processors. Illustrates commands of the DEBUG program and how to assemble, disassemble, load, save, execute, and debug programs on the IBM PC. Introduces the contents of the 8088's instruction set. Explores practical implementation techniques, covering the use of latches, transceivers, buffers, and programmable logic devices in the memory and I/O interfaces of the microcomputer system. A valuable handbook for self-study in learning microprocessors, for electrical engineers, electronic technicians, and all computer programmers.

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worst microprocessor book!

By A Customer

Now that I have completed my digital design course and using this required text at San Jose State University (home of the second author), I would like to comment on it.

There are more mistakes in this 3rd edition than in the 2nd edition. Because of the errors, I found it hard to learn for two reasons. Either I've made the mistake (granted the book is correct), or that the authors and editors failed to do their jobs so I have to seek outside help. Even the solutions in the back of the book has obvious errors! I don't believe learning from and paying [this kind of money] for this book is fair to any customer! SJSU students have used the 2nd and 3rd editions as REQUIRED text and paid dearly. (I don't believe that students should be FORCED to buy a book because it is written by a faculty member, especially one this bad!)

Suggestions and recommendations to the authors to correct blatant errors and make improvements were not implemented by the 3rd edition. How can a book create errors from one edition to another? Let me give one simple example. Figure 10-13 on page 470 has the input and output configurations for the 8255 reversed in the 3r edition BUT is correct in the 2nd edition. I also believe a spelling checker was not used as there are typos throughout the book.

Granted there are new materials in the latter part of the book, the older core part of the book should be error-free by the third edition. It has been an extreme headache to study from this book. It has been frustrating, to say the least. To those who wish to waste their money and time, please buy this book. There are other texts less costly and probably better written. The authors need to be more proactive in producing the best possible product when their reputation is on the byline!

1 of 9 people found the following review helpful.

Very Interesting and organized

By Gearhead Mania

I am taking this class at FDU (where Professor Triebel is teaching this class) and I find that its a very organized text.

there may be a few errors here and there, but it also helps to simply ask the Author for help regarding any possible errors.

Professor Triebel retired from Intel and has intimate knowledge of the 8088/8086 core (as well as the new Itanium) so he's more than qualified in this field.

We are also taking the lab to accompany this class (there's a separate lab manual) and that pretty much bridges the gap between theory and practical applications. The reason being that you can learn the theory behind the instructions in the lecture, but the lab allows you to see the instructions in action.

1 of 1 people found the following review helpful.

softcover, low quality print

By ovikanobi

I have opened the book a total of 4 times. The binding already has a split and I am just waiting for pages to start to separate.

Many of the illustrations are photos of earlier Intel publication. The print is washed out and in some cases the font are of a very small size.

So far, we covered the first few chapters, and there seems to be much devoted to simple opcodes as MOV, but not enough detail on the various adjustment opcodes.

The book has a good collection of study problems (homework).

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